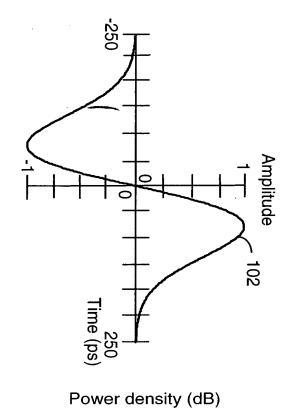


FIG. 1A



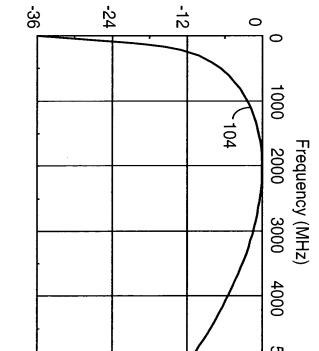
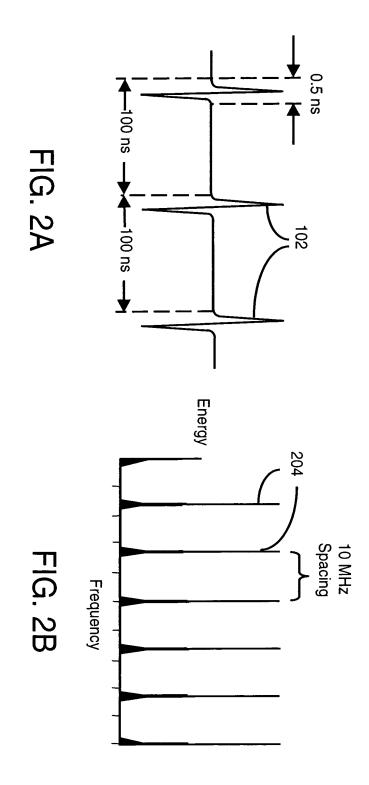
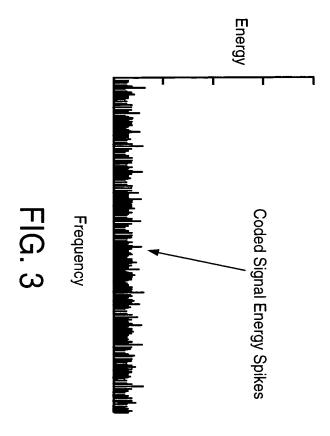
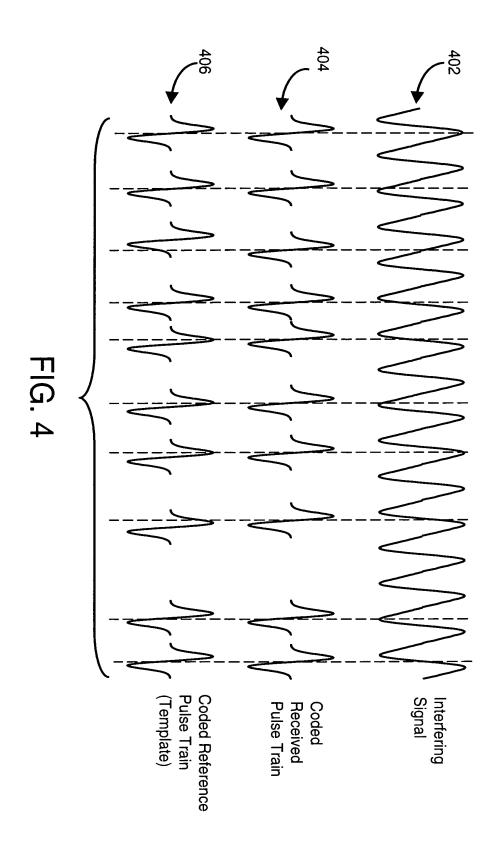


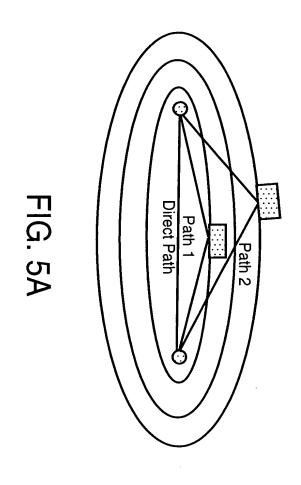
FIG. 1E

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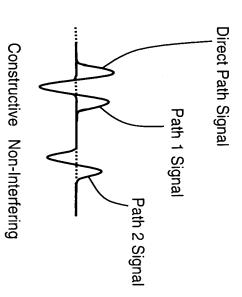
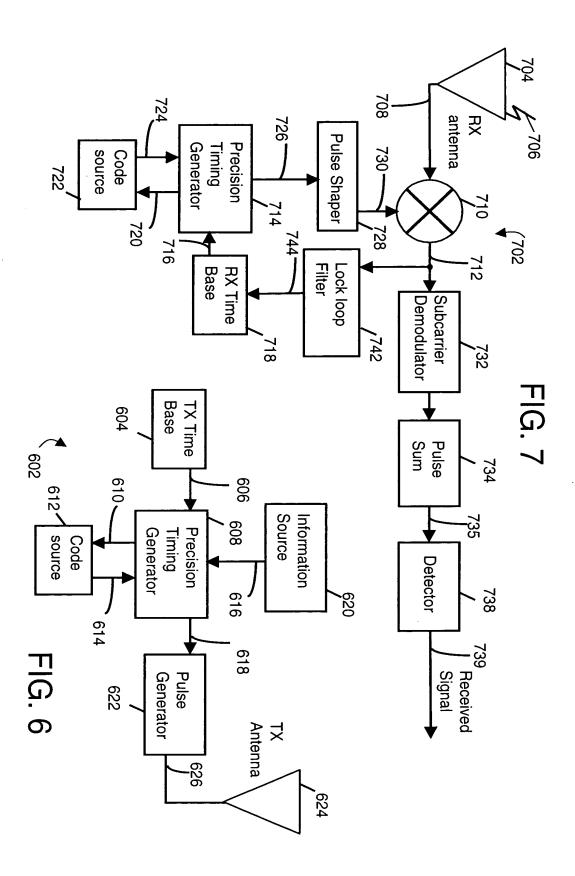
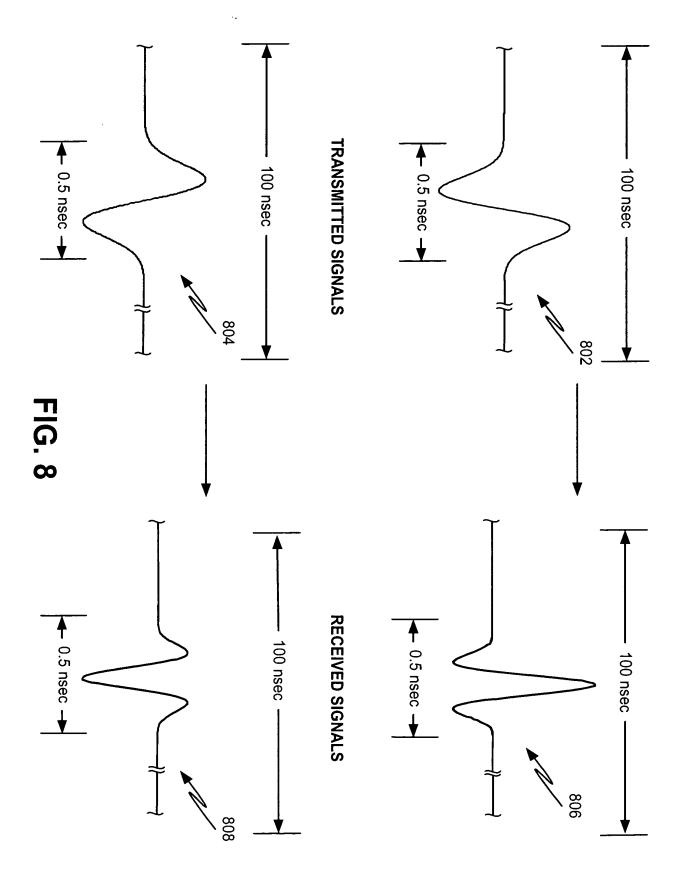
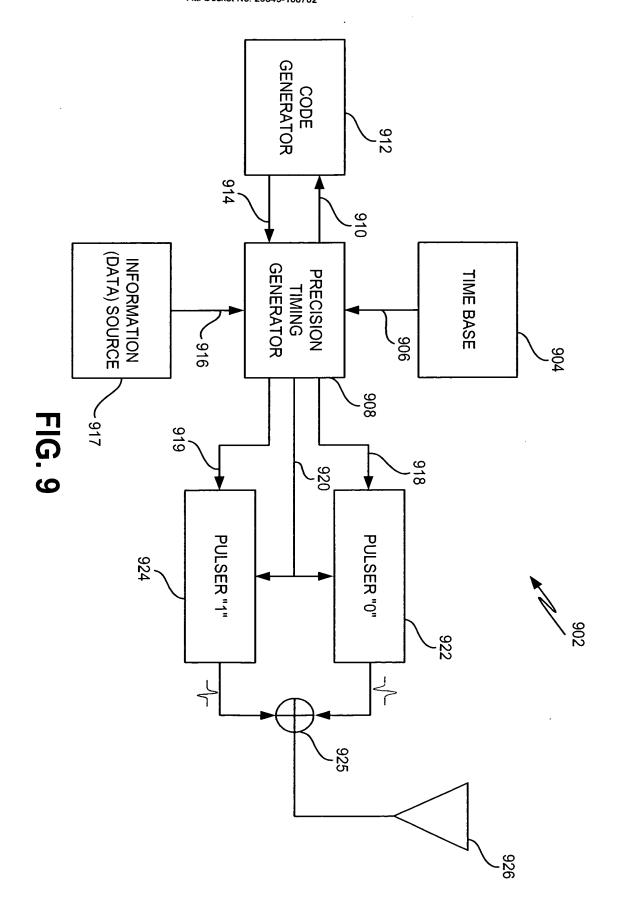


FIG. 5B







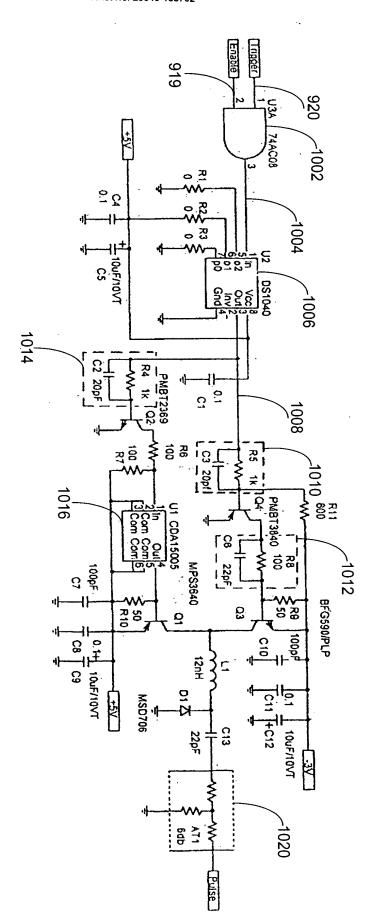


FIG. 10A

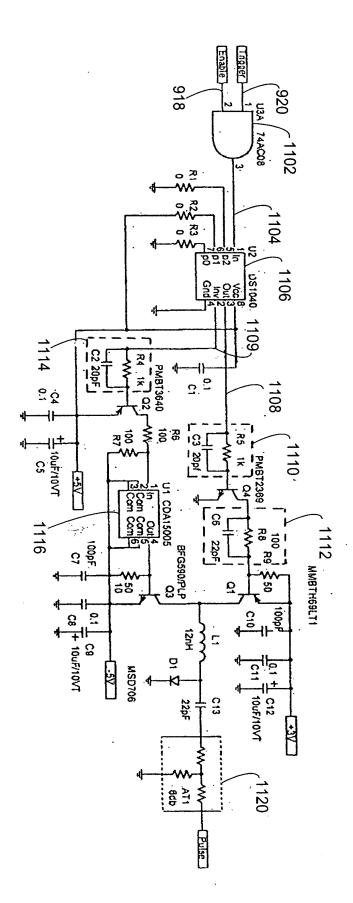
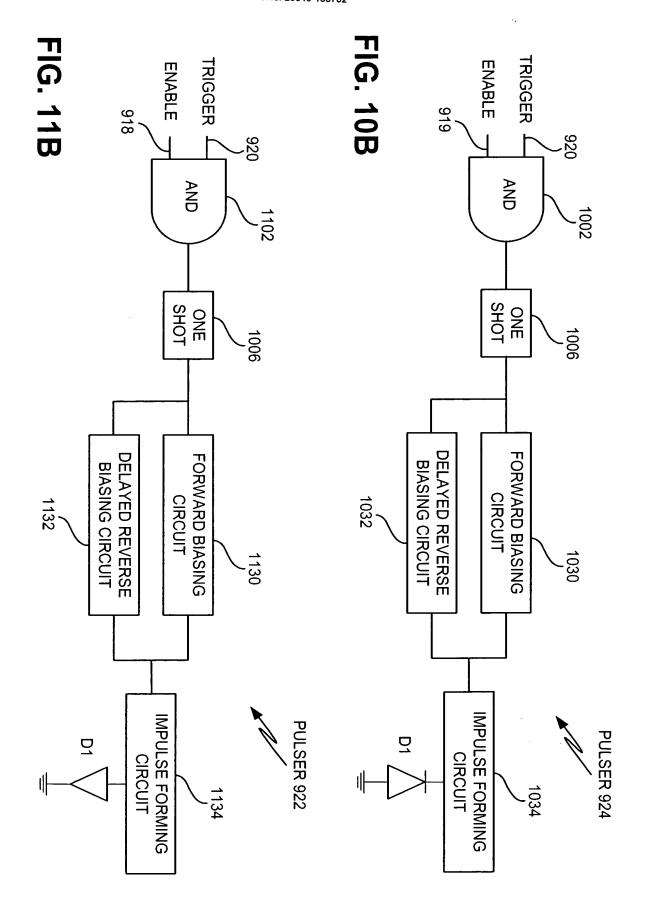
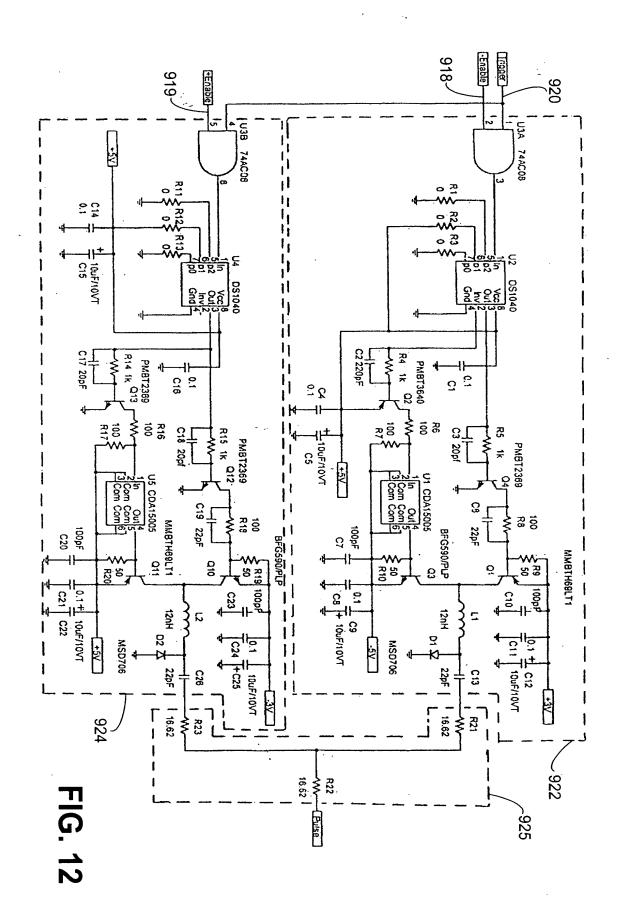
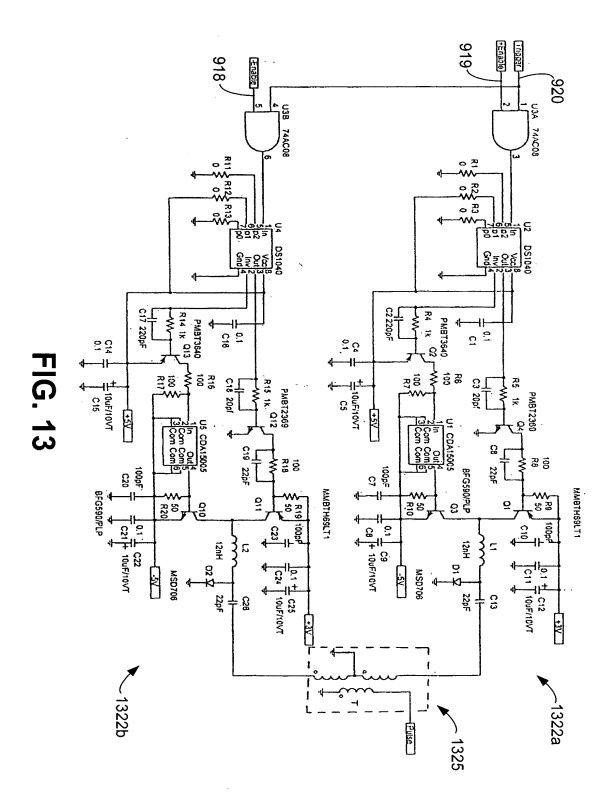


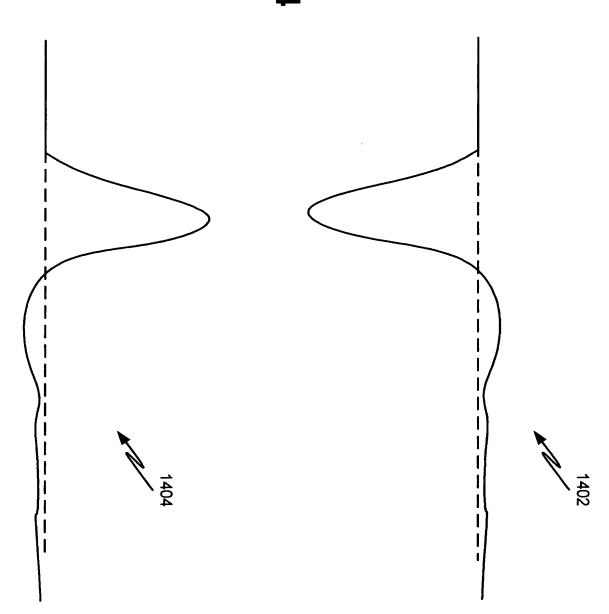
FIG.11A





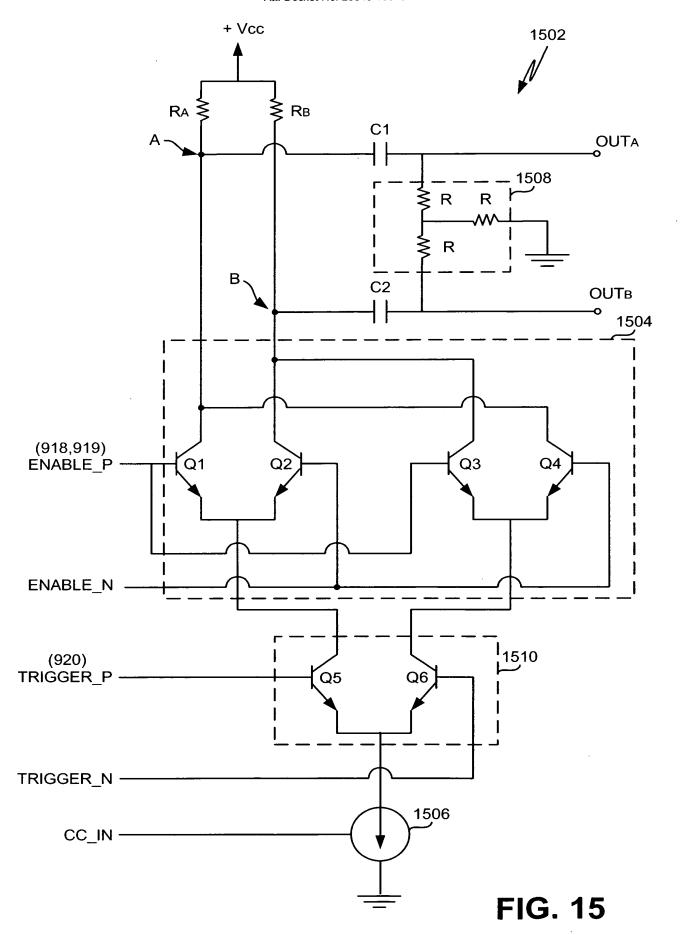


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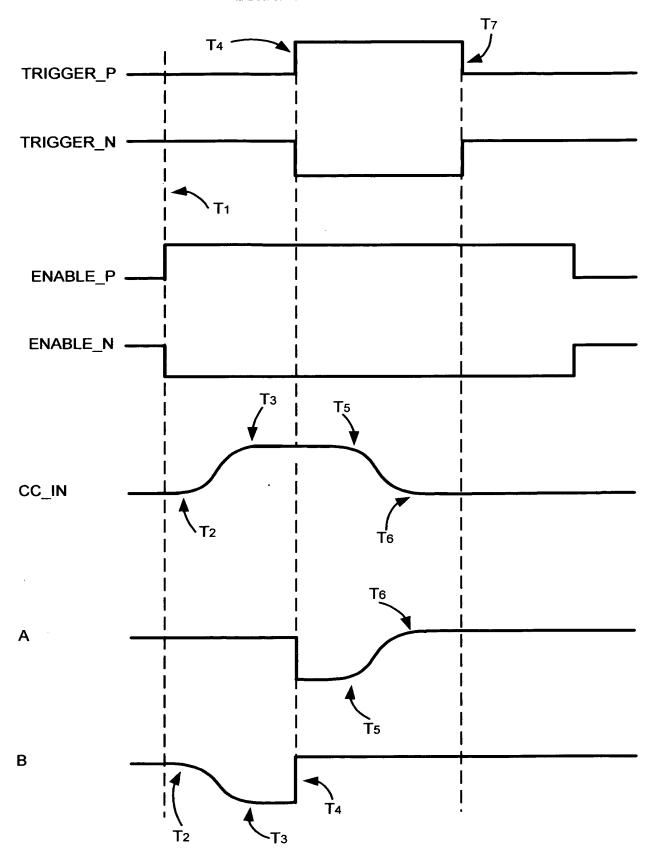
Title: APPARATUS, SYSTEM AND METHOD FOR FLIP MODULATION IN AN IMPULSE RADIO COMMUNICATIONS SYSTEM

Inventor: Larry W. Fullerton et al. Att. Docket No. 28549-188702



Title: APPARATUS, SYSTEM AND METHOD FOR FLIP MODULATION IN AN IMPULSE RADIO COMMUNICATIONS SYSTEM

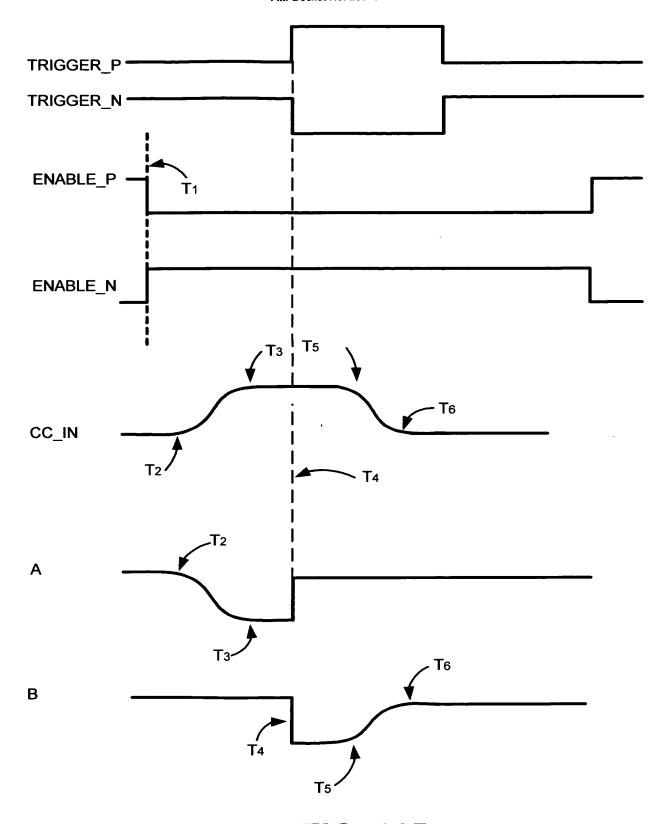
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**FIG. 16A** 

Title: APPARATUS, SYSTEM AND METHOD FOR FLIP MODULATION IN AN IMPULSE RADIO COMMUNICATIONS SYSTEM

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**FIG. 16B** 

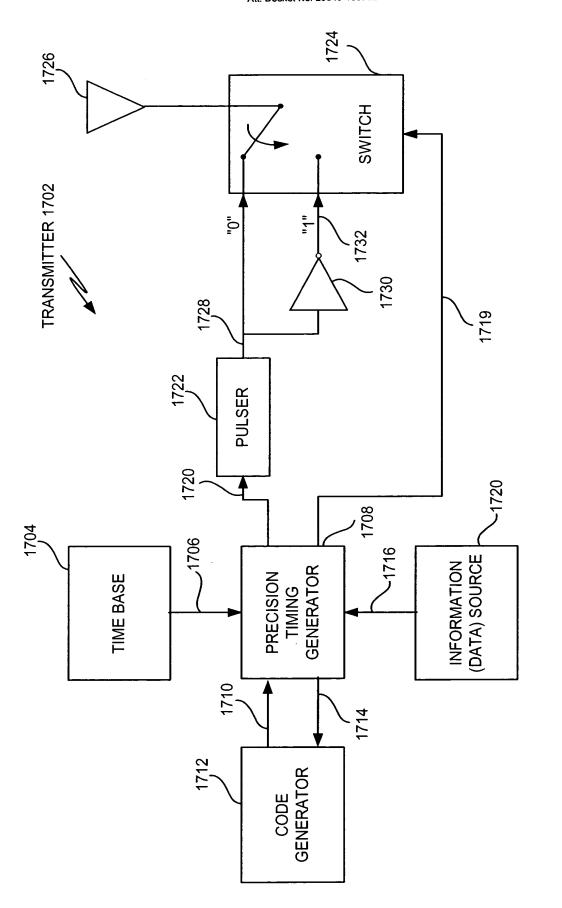


FIG. 17

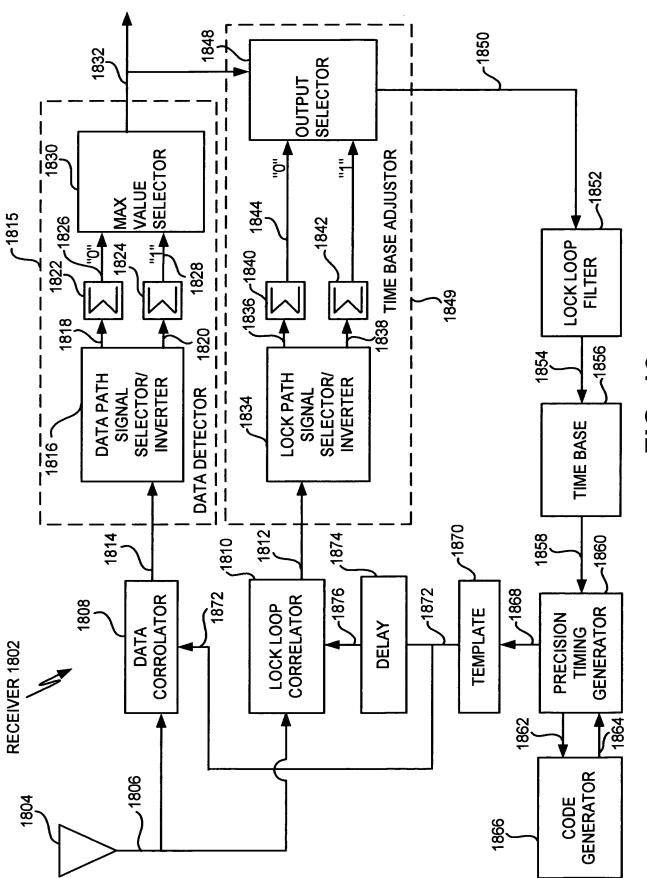
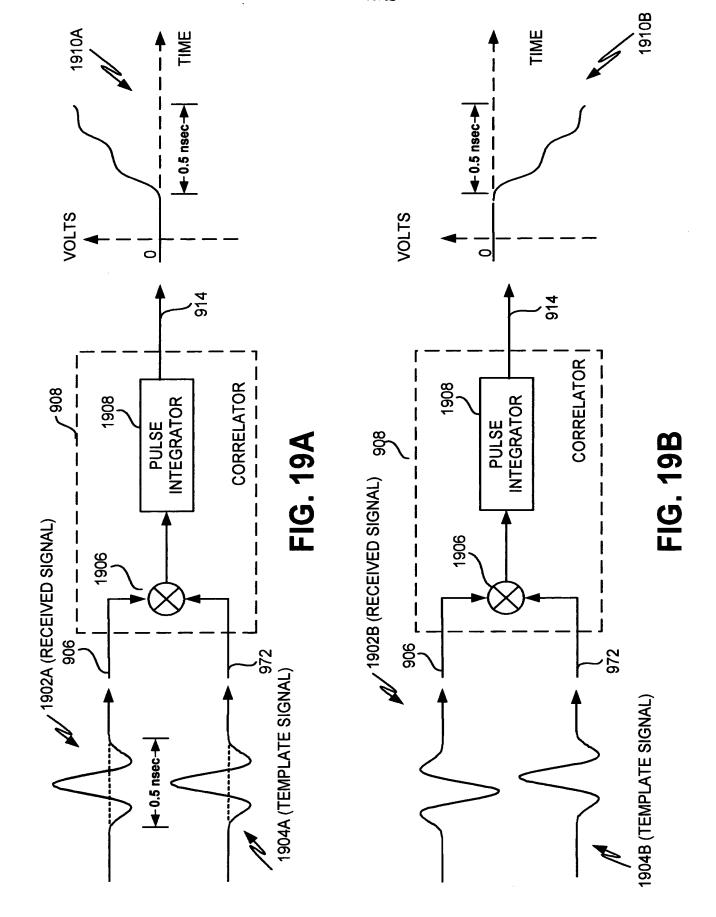
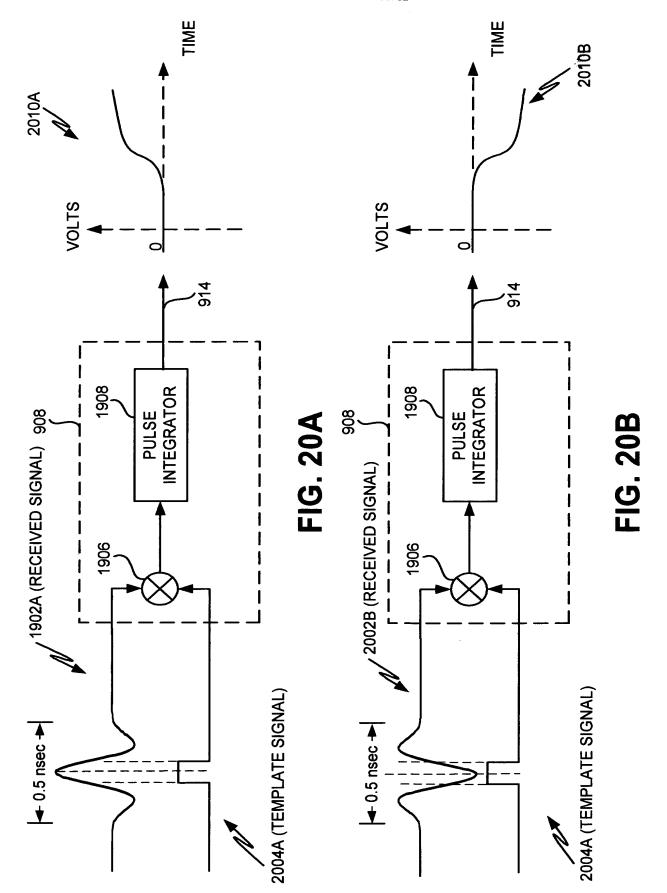
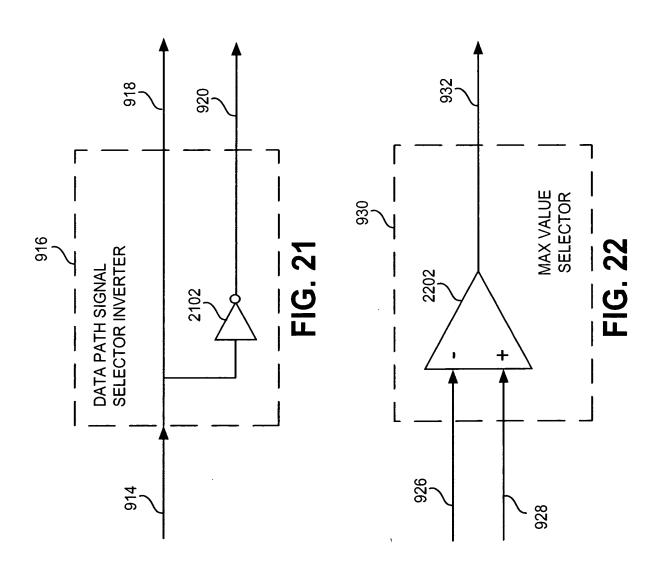
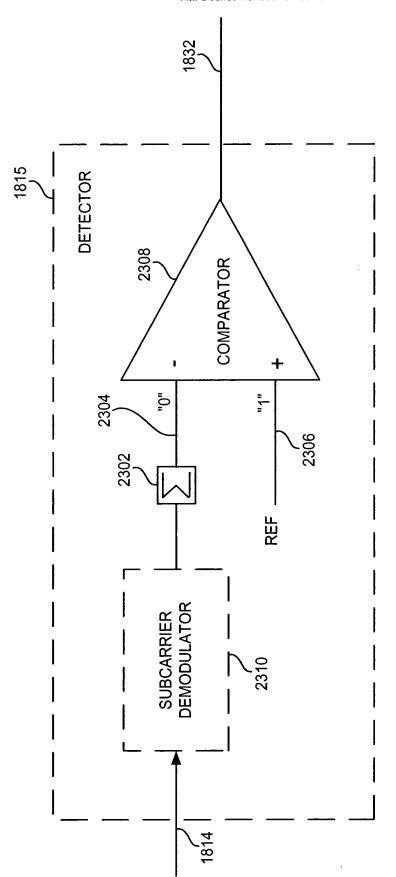


FIG. 18









Title: APPARATUS, SYSTEM AND METHOD FOR FLIP MODULATION IN AN IMPULSE RADIO COMMUNICATIONS SYSTEM Inventor: Larry W. Fullerton et al. Att. Docket No. 28549-188702 1832 FROM MAX
VALUE SELECTOR 1850 **SWITCH** 2404 OUTPUT SELECTOR 1848 1844 1846 1840 1842 1836 1838 LOCK PATH SIGNAL SELECTOR/INVERTER 2402 FROM LOCK LOOP CORRELATOR 1812

FIG. 24

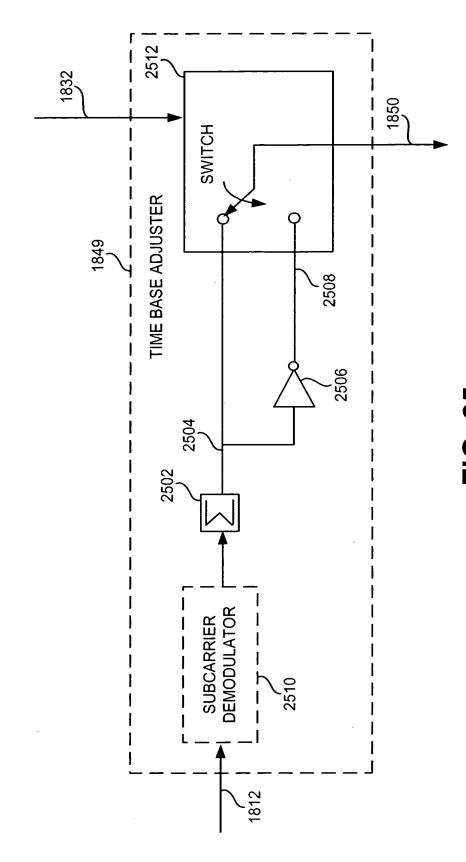
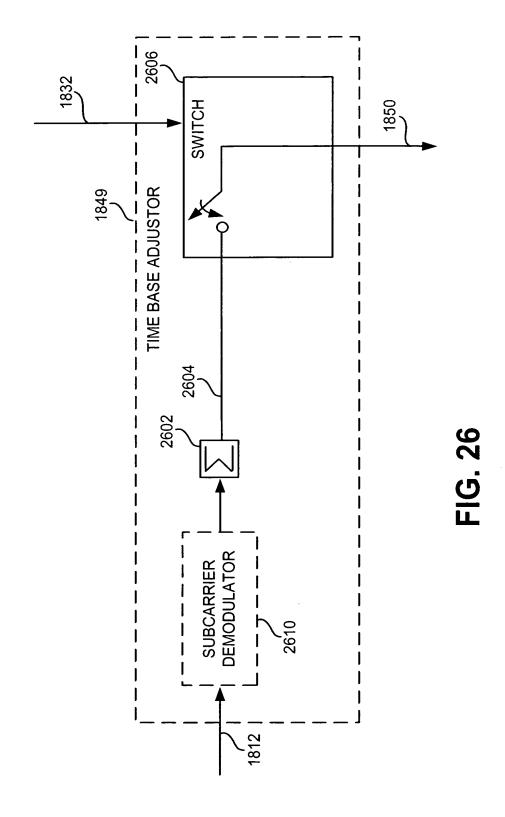
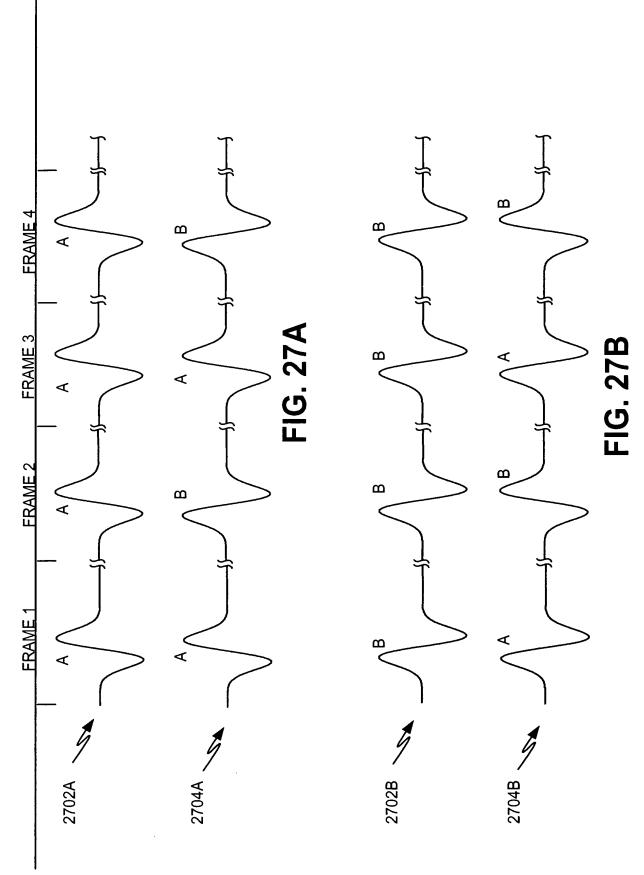
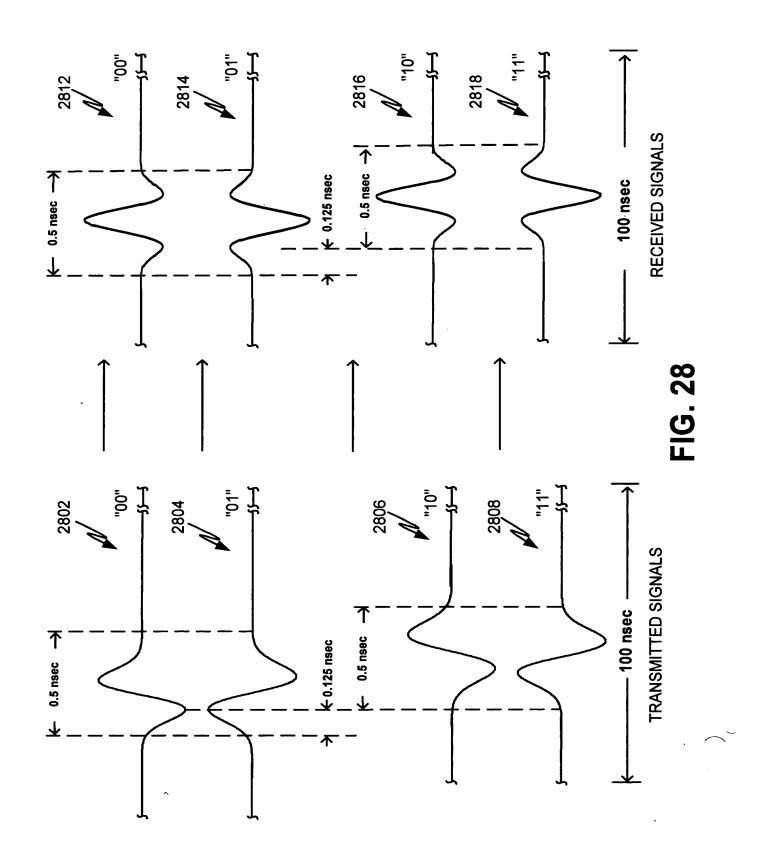


FIG. 25







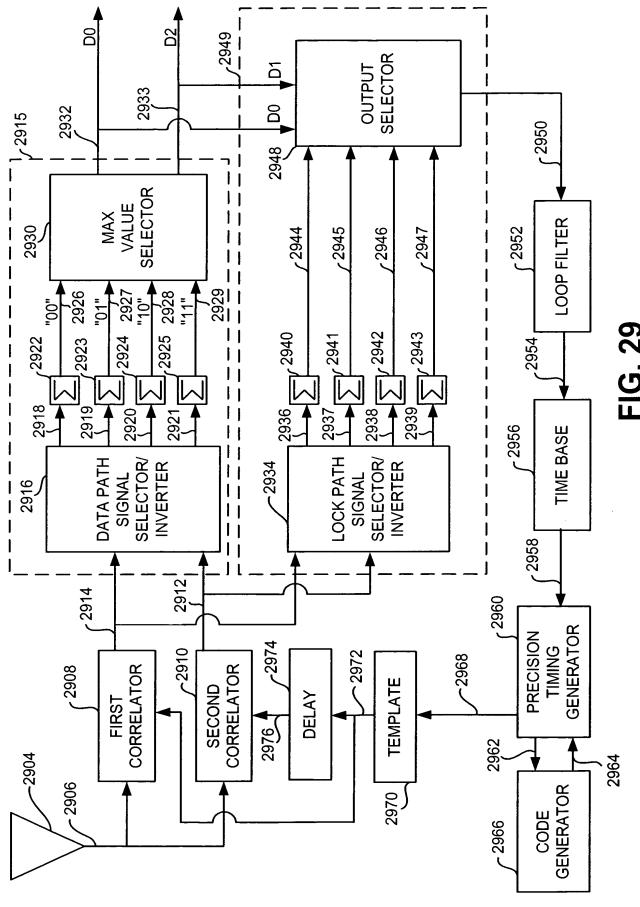
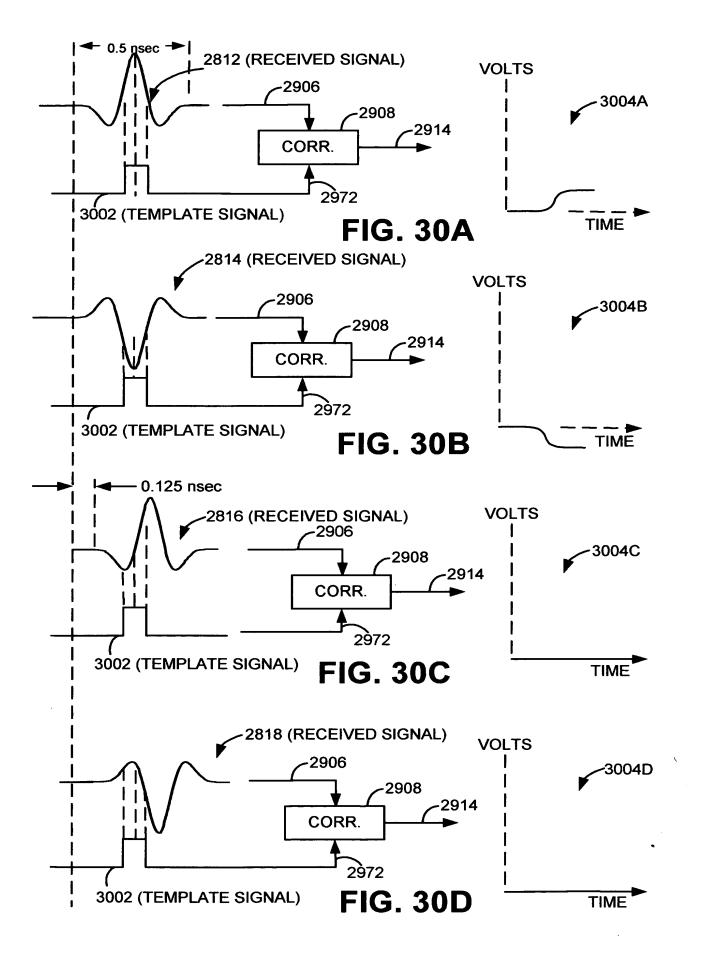
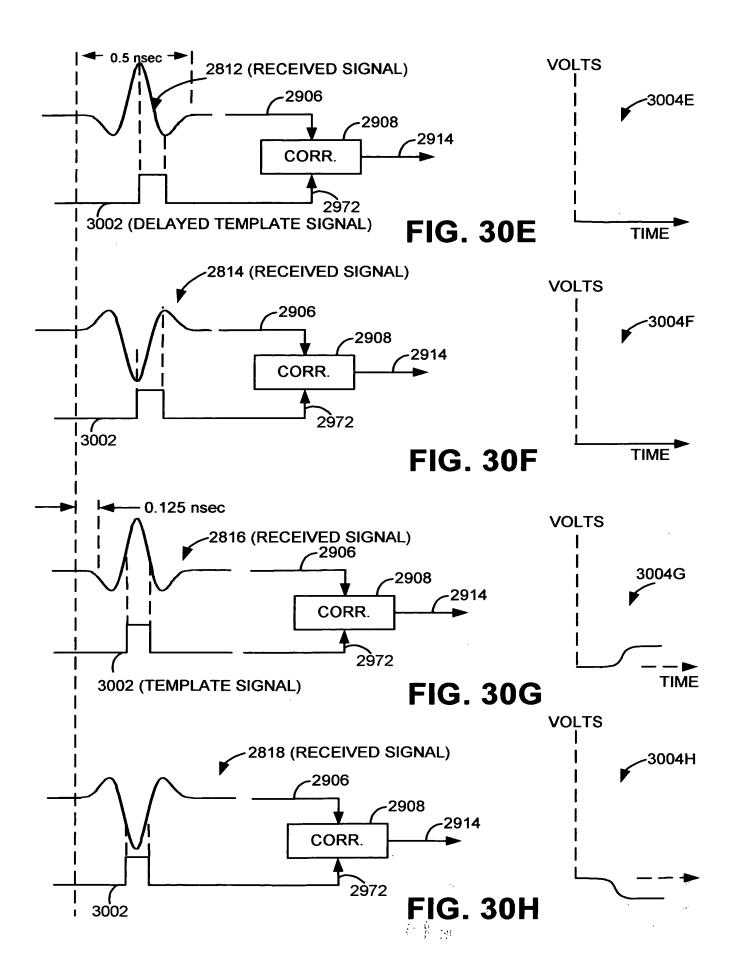
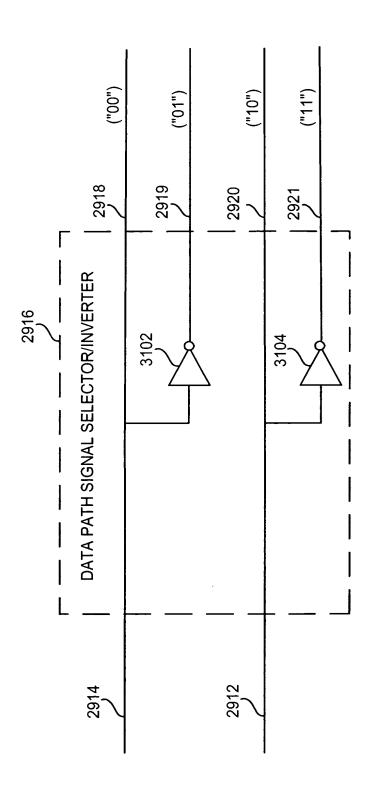


FIG. 29







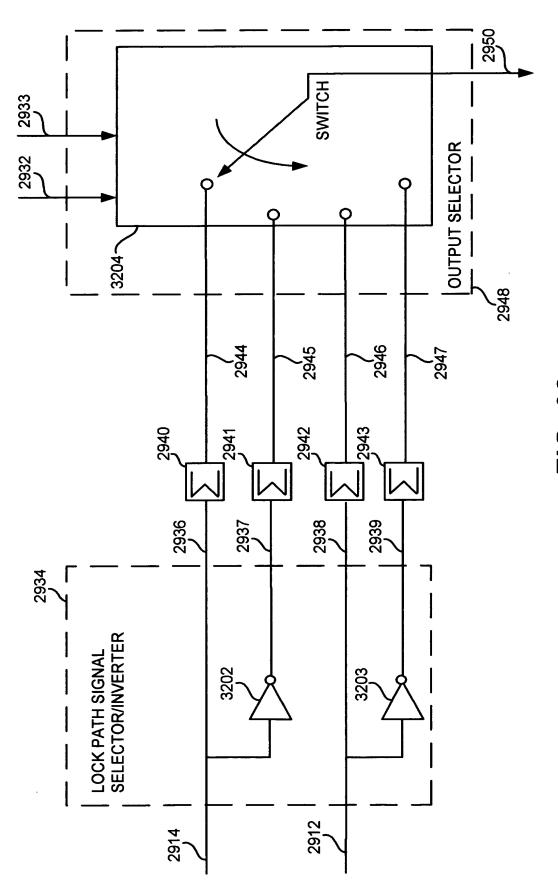


FIG. 32

